

FALL 2012 PROJECT PRESENTATION ON DEC 7th, 2012 -- ENGR345

NAME	TITLE	ADVISOR	TIME	SECTION
Lokesh Malhotra & Abhishek Munjal	"FPGA Design of Black Scholes's Model for Financial Modeling"	Prof. Chang Choo	12:30 to 12:55	1
Yinyuan Tang	"Web Embedded Platform for Medical Imaging Applications"	Prof. Harry Li	12:55 to 13:20	1
Eshaan Agarwal & Mehul Patel	"Parallel computing using Multi-core NIOS II Processers on FPGA"	Prof. Chang Choo	13:20 to 13:45	1
Maruthi Bathina	"Verilog Modeling of Bump Detection Algorithm for Machine Vision"	Prof. Chang Choo	13:45 to 14:10	2
Sharath Raju	"Digital LNBC for Radio Astronomy"	Dr. Shahab Ardalan	14:10 to 14:35	2
Tarun Goyal	"Low Power ALU design Using combination of static and pass logic"	Dr. S. Hamed Hagh	14:35 to 15:00	2
Mohammad Jafarinejad	"Noise Cancellation of Floor Vibrations in Testing Noise Levels of Micro-Electro-Mechanical- Systems Inertial Sensors"	Prof. Avtar Singh	15:00 to 15:25	3
Zhisen Li & Anushree Pandit	"Enhance Security Remote Reboot Management System (ESRRMS)"	Prof Morris Jones	15:25 to 15:50	3
Sepehr Halali	FPGA Implementation of NAND Flash Memory Error Correction using a BCH Code in an FPGA"	Prof. Morelos-Zaragosa	15:50 to 16:15	3