EE275, Advanced Computer Architecture
Fall 2017

Instructor: Dr. Tri Caohuu
Office Location: ENG 375
Telephone: 408-924-3951
Email: tri.caohuu@sjsu.edu
Office Hours: MW 14:00 – 15:30
Class Days/Time: MW 18:00 to 19:15
Classroom: DMH 149B
Prerequisites: EE270

Course Description
Performance metrics, instruction set architectures, instruction pipelining and pipeline hazards, instruction-level parallelism, multithreading, cache and virtual memory, I/O performance and advanced topics in storage systems, topologies and hardware/software issues of interconnection networks.

Required Texts/Readings
Textbook

Other Readings

Classroom Protocol
- Students are encouraged to ask questions in the class.
- Each student is required to engage in classroom activities, submit assignments and reports on time, and take exams and tests on time.
- Web-browsing in class is not allowed. Cell Phones are to be turned off during lectures and tests. During exams if you receive a cell phone call or page it will be
assumed that you have completed your exam and no further work will be allowed. (More details in “Additional Policies and Procedures” section)

- No make-up exams will be held.
- Student causing disruption in the class will be asked to leave the class

**E-Learning Platform**

All course materials, including lecture slides and course assignments, are hosted by San Jose State University’s chosen e-learning platform – Desire2Learn (D2L). You can go to site: https://sjsu.desire2learn.com/ to log into your account. If you don’t know your login information yet, see the resources for students: http://www.sjsu.edu/ecampus/students/

Students are encouraged to use the D2L discussion boards that have been created for collaboration.

**Dropping and Adding**

Students are responsible for understanding the policies and procedures about add/drop, grade forgiveness, etc. Refer to the current semester’s Catalog Policies section at http://info.sjsu.edu/static/catalog/policies.html. Add/drop deadlines can be found on the current academic calendar web page at http://www.sjsu.edu/provost/Academic_Calendars/. The Late Drop Policy is available at http://www.sjsu.edu/aars/policies/latedrops/policy/.

Students should be aware of the current deadlines and penalties for dropping classes.

Information about the latest changes and news is available at the Advising Hub at http://www.sjsu.edu/advising/.

**Assignments and Grading Policy**

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<tr>
<th>Assignment</th>
<th>Percentage</th>
</tr>
</thead>
<tbody>
<tr>
<td>Homework/Quiz/Participation</td>
<td>10%</td>
</tr>
<tr>
<td>Project</td>
<td>25%</td>
</tr>
<tr>
<td>Midterm Exam</td>
<td>25%</td>
</tr>
<tr>
<td>Final Exam</td>
<td>40%</td>
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</tbody>
</table>

The instructor reserves the right to change the percentages

*Failure to complete and submit 90% of homework and project assignments will result in a failing grade in this class.*

**Notes:**

Homework assignments are due one week from the assigned date. Late homework will not be accepted.
Grading
Grade   Overall Score
A+   95-100
A   90-94
B+   85-89
B   80-84
C+   75-79
C   70-74
D+   65-69
D   60-64
F   0-59

Late Penalty
Assignments submitted after the deadline carry 10% deduction every day.

Makeup Exam
NO makeup exams will be given unless you have documented compelling reasons.

University Policies
Academic integrity
Your commitment as a student to learning is evidenced by your enrollment at San Jose State University. The University’s Academic Integrity policy, located at http://www.sjsu.edu/senate/S07-2.htm, requires you to be honest in all your academic course work. Faculty members are required to report all infractions to the office of Student Conduct and Ethical Development. The Student Conduct and Ethical Development website is available at http://www.sjsu.edu/studentconduct/.

Instances of academic dishonesty will not be tolerated. Cheating on exams or plagiarism (presenting the work of another as your own, or the use of another person’s ideas without giving proper credit) will result in a failing grade and sanctions by the University. For this class, all assignments are to be completed by the individual student unless otherwise specified. If you would like to include your assignment or any material you have submitted, or plan to submit for another class, please note that SJSU’s Academic Integrity Policy S07-2 requires approval of instructors.

Campus Policy in Compliance with the American Disabilities Act
If you need course adaptations or accommodations because of a disability, or if you need to make special arrangements in case the building must be evacuated, please make an appointment with me as soon as possible, or see me during office hours. Presidential Directive 97-03 at http://www.sjsu.edu/president/docs/directives/PD_1997-03.pdf requires that students with disabilities requesting accommodations must register with the
Disability Resource Center (DRC) at http://www.drc.sjsu.edu/ to establish a record of their disability.

Student Technology Resources (Optional)

Computer labs for student use are available in the Academic Success Center (http://www.at.sjsu.edu/asc/) located on the 1st floor of Clark Hall and in the Associated Students Lab on the 2nd floor of the Student Union. Additional computer labs may be available in your department/college. Computers are also available in the Martin Luther King Library.

A wide variety of audio-visual equipment is available for student checkout from Media Services located in IRC 112. These items include DV and HD digital camcorders; digital still cameras; video, slide and overhead projectors; DVD, CD, and audiotape players; sound systems, wireless microphones, projection screens and monitors.

SJSU Peer Connections (Optional)

The Learning Assistance Resource Center (LARC) and the Peer Mentor Program have merged to become Peer Connections. Peer Connections is the new campus-wide resource for mentoring and tutoring. Our staff is here to inspire students to develop their potential as independent learners while they learn to successfully navigate through their university experience. Students are encouraged to take advantage of our services which include course-content based tutoring, enhanced study and time management skills, more effective critical thinking strategies, decision making and problem-solving abilities, and campus resource referrals.

In addition to offering small group, individual, and drop-in tutoring for a number of undergraduate courses, consultation with mentors is available on a drop-in or by appointment basis. Workshops are offered on a wide variety of topics including preparing for the Writing Skills Test (WST), improving your learning and memory, alleviating procrastination, surviving your first semester at SJSU, and other related topics. A computer lab and study space are also available for student use in Room 600 of Student Services Center (SSC).

Peer Connections is located in three locations: SSC, Room 600 (10th Street Garage on the corner of 10th and San Fernando Street), at the 1st floor entrance of Clark Hall, and in the Living Learning Center (LLC) in Campus Village Housing Building B. Visit Peer Connections website at http://peerconnections.sjsu.edu for more information.

SJSU Writing Center (Optional)

The SJSU Writing Center is located in Suite 126 in Clark Hall. It is staffed by professional instructors and upper-division or graduate-level writing specialists from each of the seven SJSU colleges. Our writing specialists have met a rigorous GPA requirement, and they are well trained to assist all students at all levels within all
disciplines to become better writers. The Writing Center staff can be found at http://www.sjsu.edu/writingcenter/about/staff/.
## Course Schedule

The tentative schedule is subject to change.

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<tr>
<th>Week</th>
<th>Topics</th>
<th>Textbook</th>
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<td>1</td>
<td>8/23 Class introduction</td>
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<tr>
<td>2</td>
<td>8/28, 30 Fundamentals of Quantitative Design and Analysis</td>
<td>Chapter 1</td>
</tr>
<tr>
<td>3</td>
<td>9/4, 6 Labor Day Fundamentals of Quantitative Design and Analysis</td>
<td>Chapter 1 Appendix A</td>
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<tr>
<td>4</td>
<td>9/11, 13 Fundamentals of Quantitative Design and Analysis</td>
<td>Appendix A Appendix B</td>
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<td>5</td>
<td>9/18, 20 Instruction set principles. Instruction set principles (A1&amp;A2)</td>
<td>Appendix B Chapter 2</td>
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<td>6</td>
<td>9/25, 27 Review of Memory Hierarchy (B1&amp;B2)</td>
<td>Chapter 2</td>
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<td>7</td>
<td>10/2, 4 Memory Hierarchy Design</td>
<td>Appendix C</td>
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<td>8</td>
<td>10/9, 11 Pipelining: Basic and Intermediate Concepts</td>
<td>Chapter 3</td>
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<td>9</td>
<td>10/16, 18 Instruction-Level Parallelism and It’s Exploitation</td>
<td>Chapter 3</td>
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<tr>
<td>10</td>
<td>10/23, 25 Review for Midterm</td>
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<td></td>
<td><strong>MIDTERM 10/25</strong></td>
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<td>11</td>
<td>10/30, 11/1 Instruction-Level Parallelism and It’s Exploitation</td>
<td>Chapter 5</td>
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<tr>
<td>Week</td>
<td>Topics</td>
<td>Textbook</td>
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<tr>
<td>12</td>
<td>11/6,8 Thread-Level Parallelism (5a)</td>
<td>Chapter 5</td>
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<td>13</td>
<td>11/13,15 Thread-Level Parallelism (5b)</td>
<td>Chapter 4</td>
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<tr>
<td>14</td>
<td>11/20,22 No-instruction day Data Level Parallelism in Vector, SIMD, and GPU Architecture (4a)</td>
<td>Chapter 4</td>
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<tr>
<td>15</td>
<td>11/27,29 Data Level Parallelism in Vector, SIMD, and GPU Architecture (4b)</td>
<td>Chapter 4</td>
</tr>
<tr>
<td>16</td>
<td>12/4,6 Data Level Parallelism in Vector, SIMD, and GPU Architecture (4b) cont.</td>
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<tr>
<td>17</td>
<td>12/11 Review for Exam</td>
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<tr>
<td>12/13</td>
<td><strong>December 13th, Wednesday, 17:15-19:30 FINAL EXAM</strong></td>
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